

IN THE CLAIMS:

Cancel claims 2-9 and 11-25.

Add claims 26 and 27 as follows.

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26. (Added) A system comprising:
a computer bus;
a host processor programmed to perform error code correction; and
a drive for providing an encryption mask, the drive performing a bitwise XOR of an encryption mask and a block of ECC-encoded data, a product of the bitwise XOR being an encrypted block; the drive providing the encrypted block to the computer bus, whereby an encrypted block can be sent to the host processor via the computer bus for error code correction.

27. (Added) A drive comprising:
a reader; and
a controller programmed to perform a bitwise XOR of an encryption mask and a block of ECC-encoded data, a product of the bitwise XOR being an encrypted block, the controller further being programmed to output the encrypted block.

REMARKS

This is a continuation of co-pending U.S. Serial No. 09/053,972 filed April 2, 1998. Claims 1,10, 26 and 27 are pending. The pending claims in this continuation application are present in U.S. Serial No. 09/053,972.

The pending claims were rejected in the parent application. On February 7, 2001 the undersigned discussed the pending claims with Examiner DiLorenzo. The undersigned indicated that the prior art cited by the examiner - Monroe, Davis and official notice that it is well known to perform XOR encryption of data strings - did not teach or suggest applying XOR encryption to ECC codewords. Thus combining the teachings of the cited art did not